

FIG. 1 is a block diagram of a data storage system 20, which includes a data access manager 28, a data storage assembly 30-A, and a data storage system 24. The data access manager 28 includes a processor 32-B and memory 34-B. The data storage assembly 30-A includes a control circuit 33 and data storage 48. The data storage system 24 includes a storage area network 26. The data access manager 28 is connected to the data storage assembly 30-A and the data storage system 24. The data storage assembly 30-A is connected to the data storage system 24. The data storage system 24 is connected to the data access manager 28.

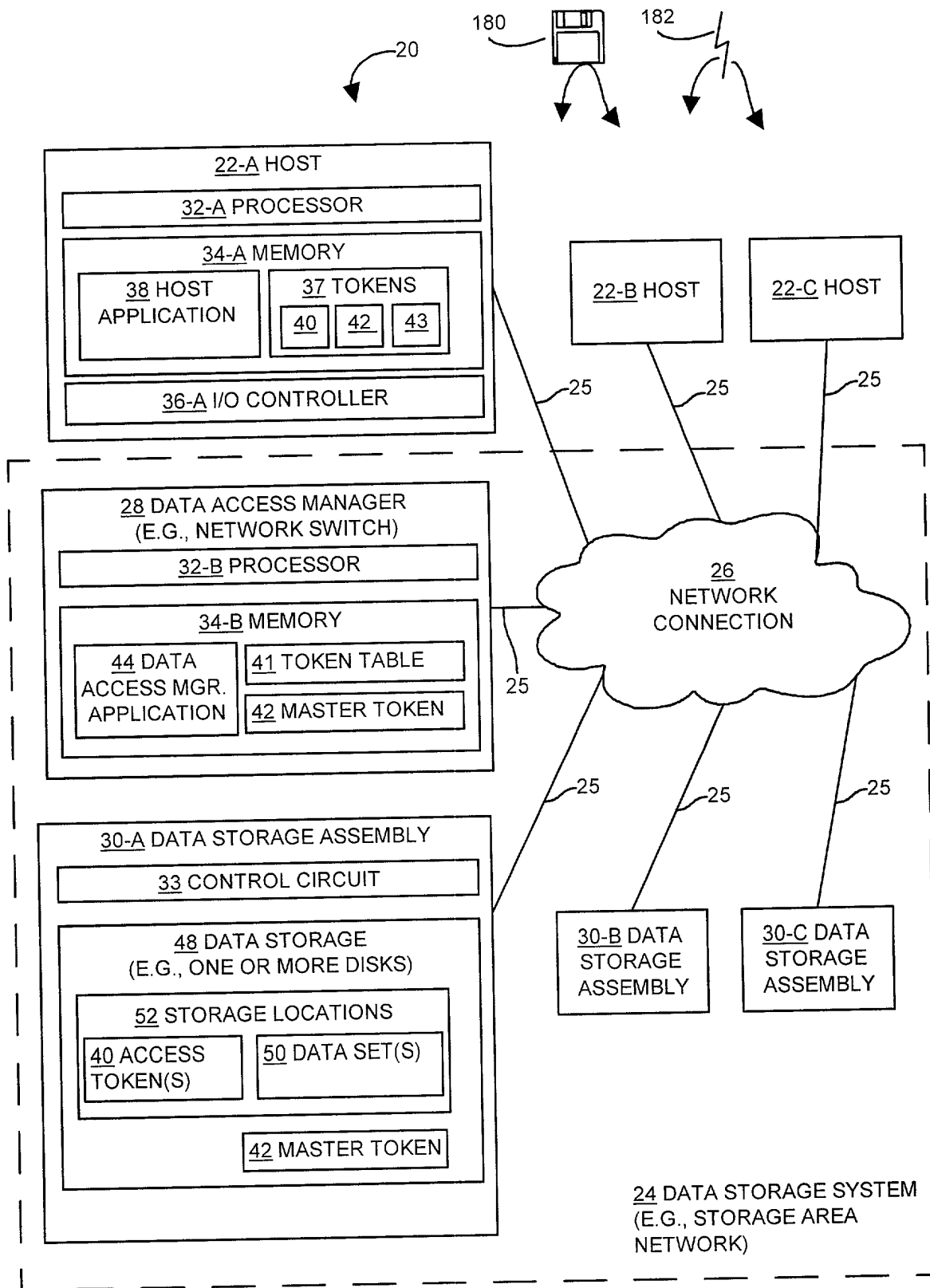


FIG. 1

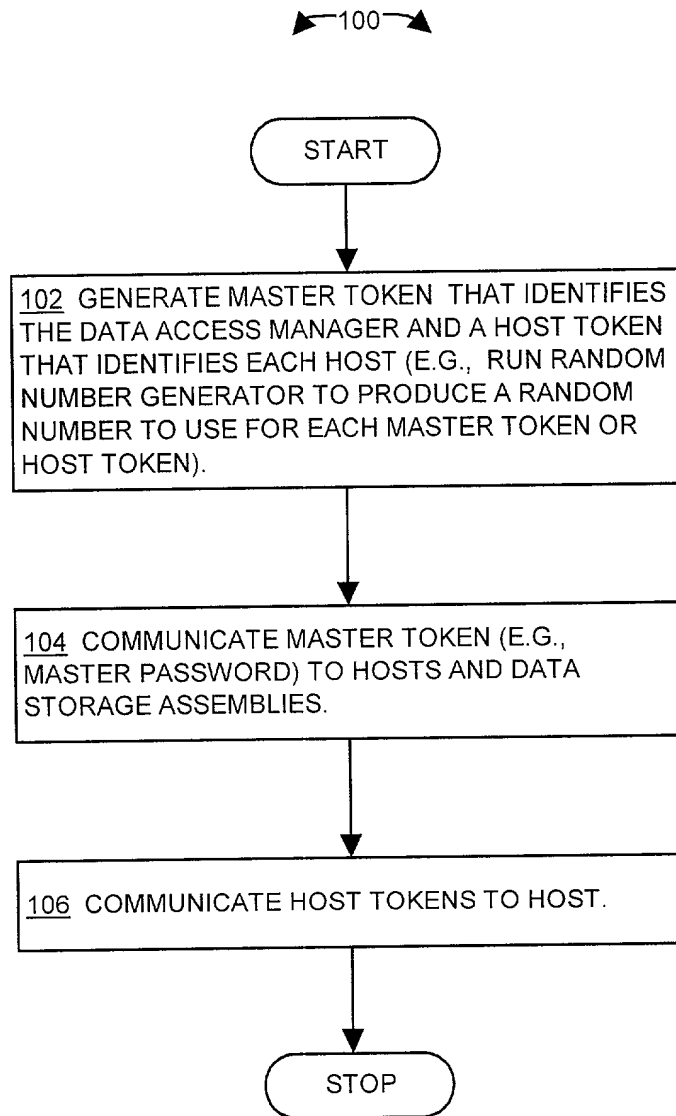


FIG. 2

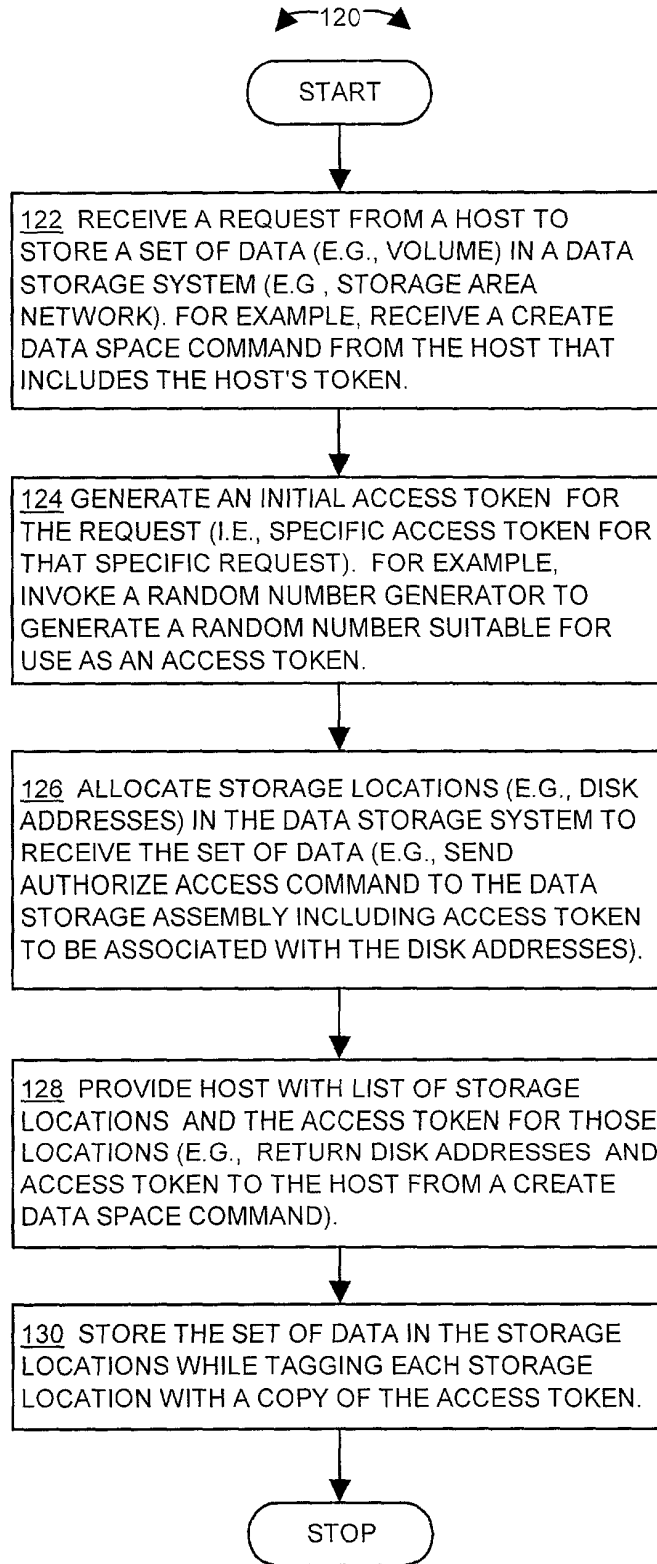


FIG. 3

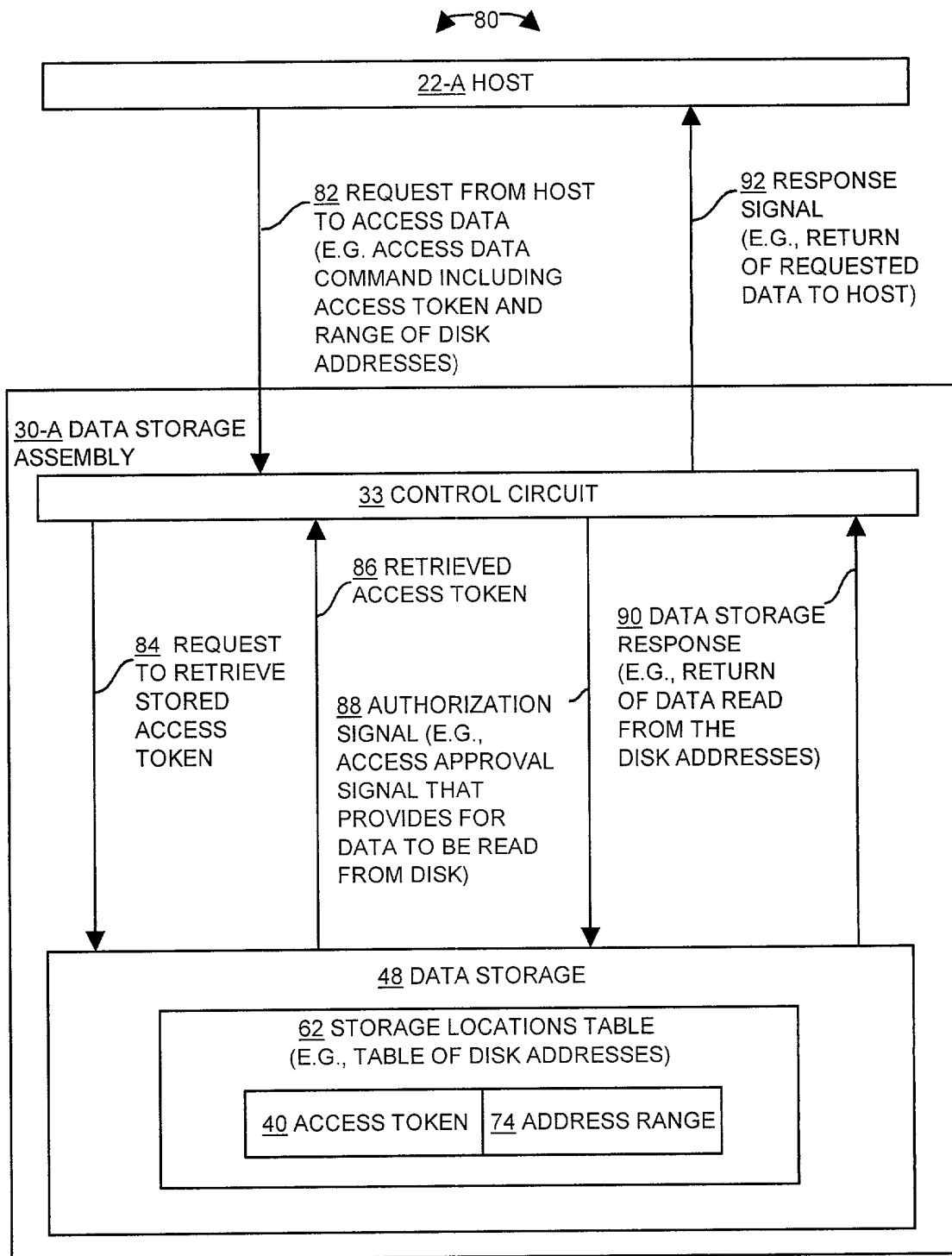


FIG. 4

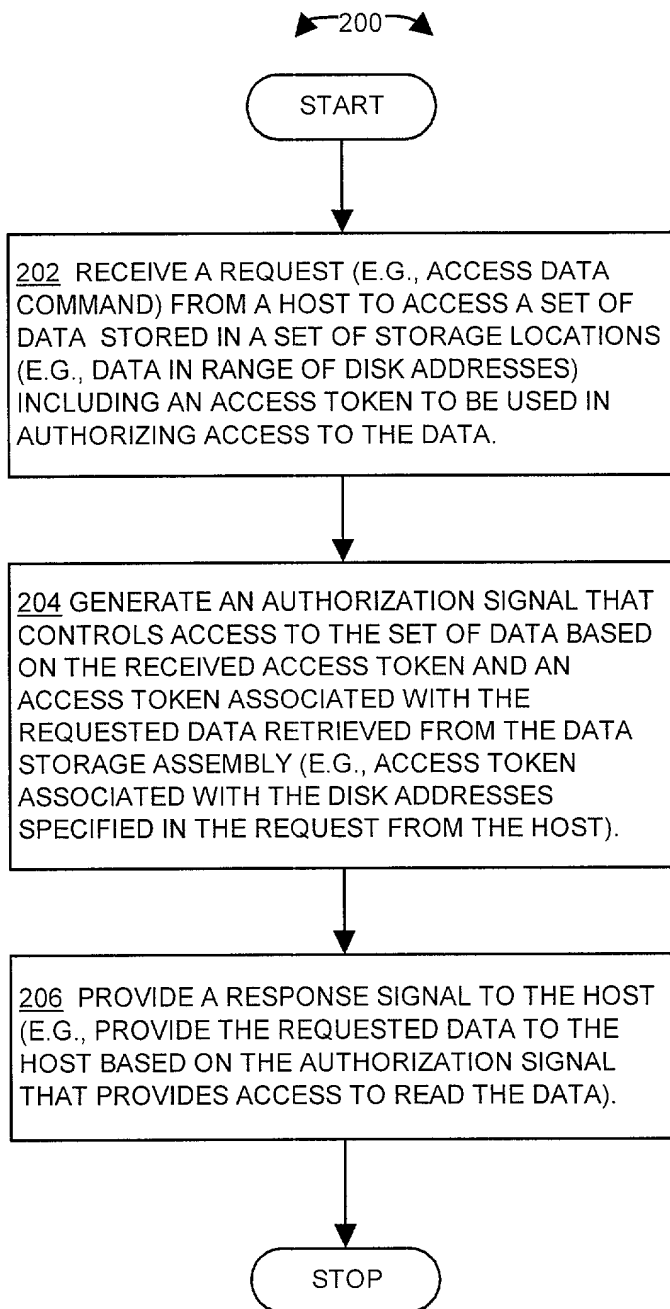


FIG. 5

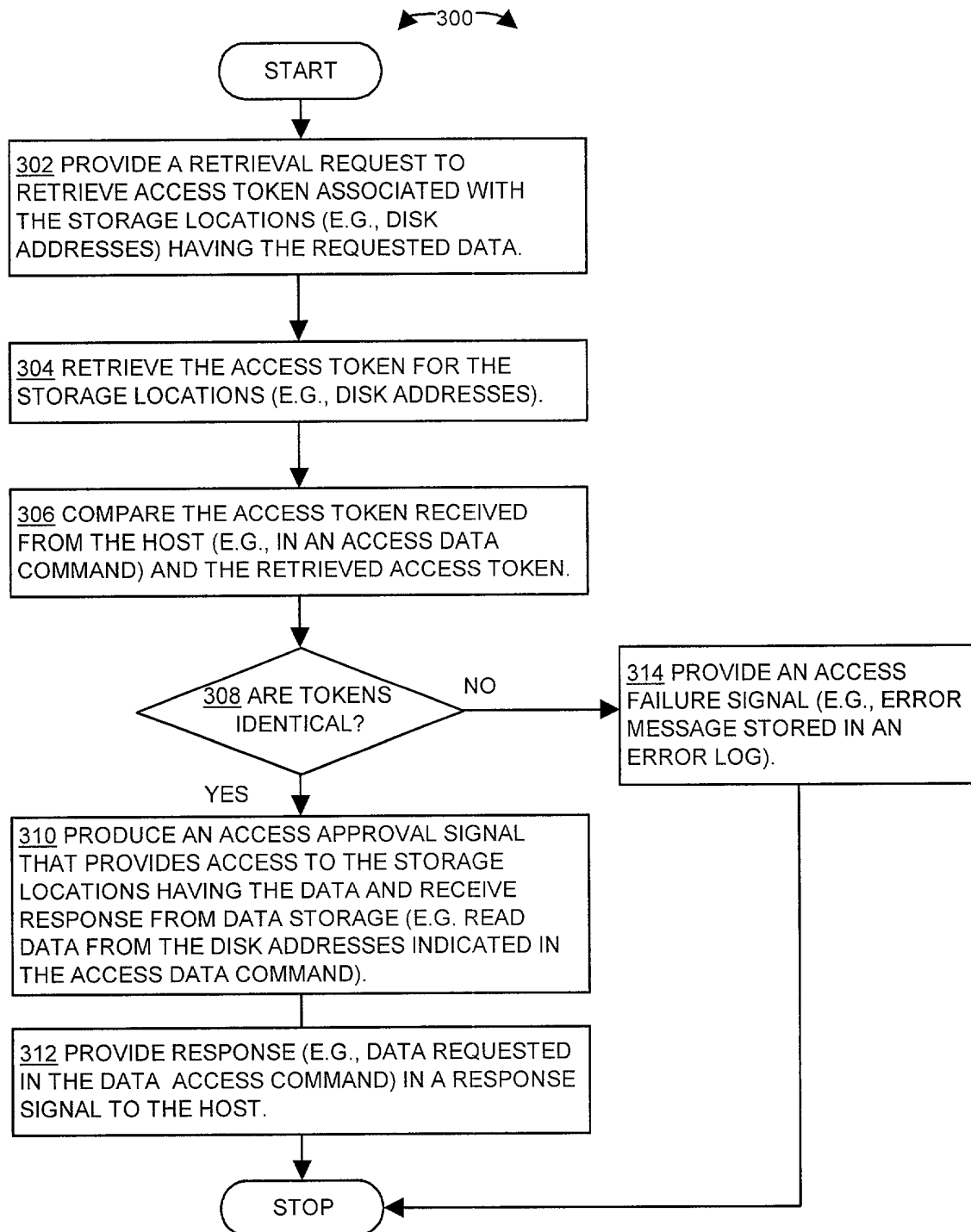


FIG. 6

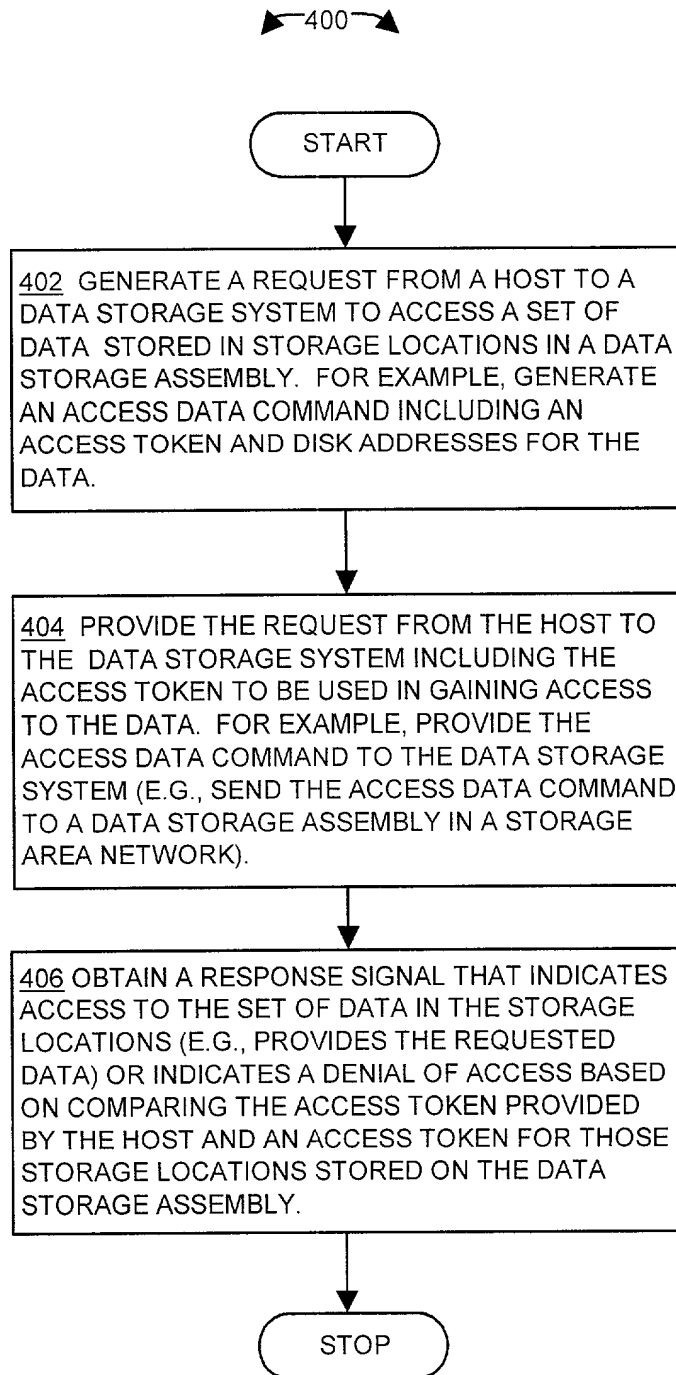


FIG. 7

500

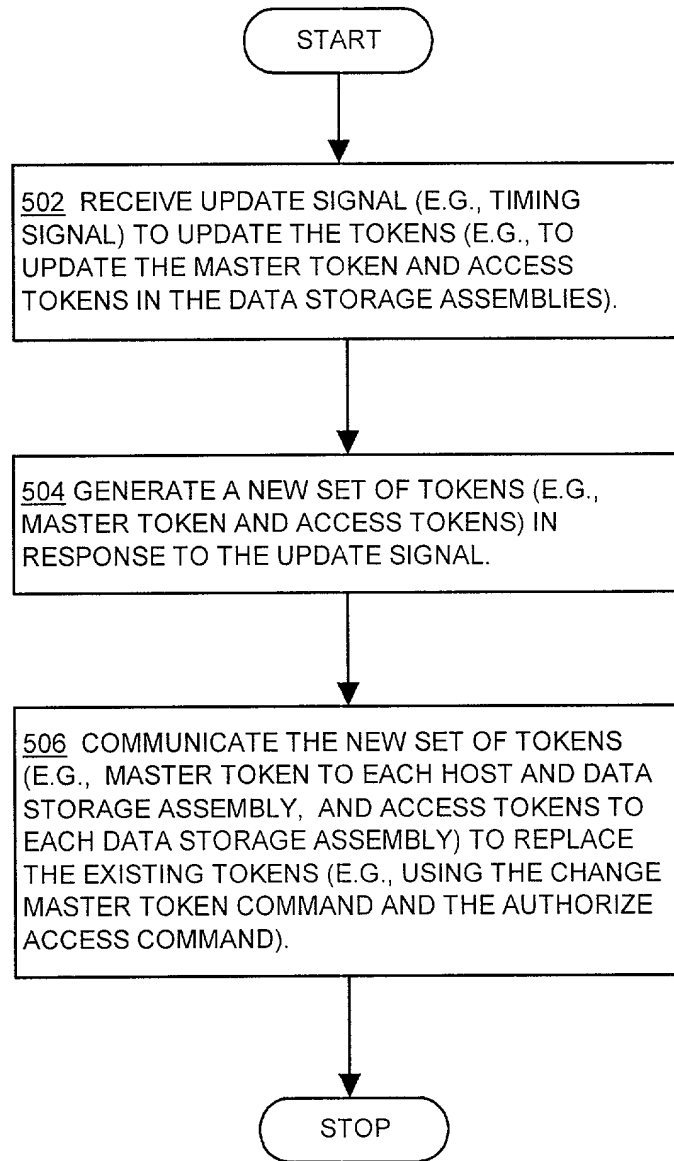


FIG. 8



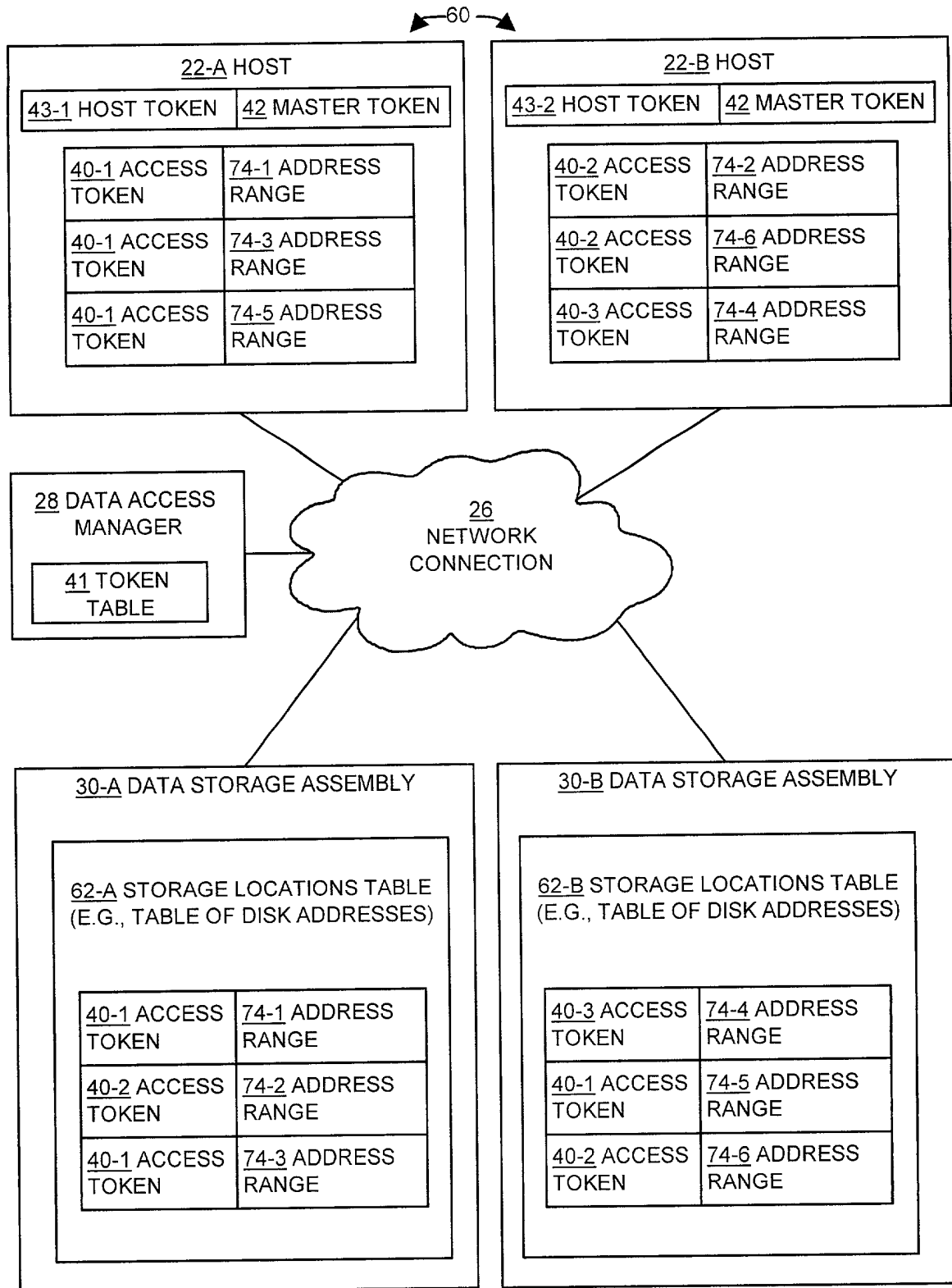


FIG. 9

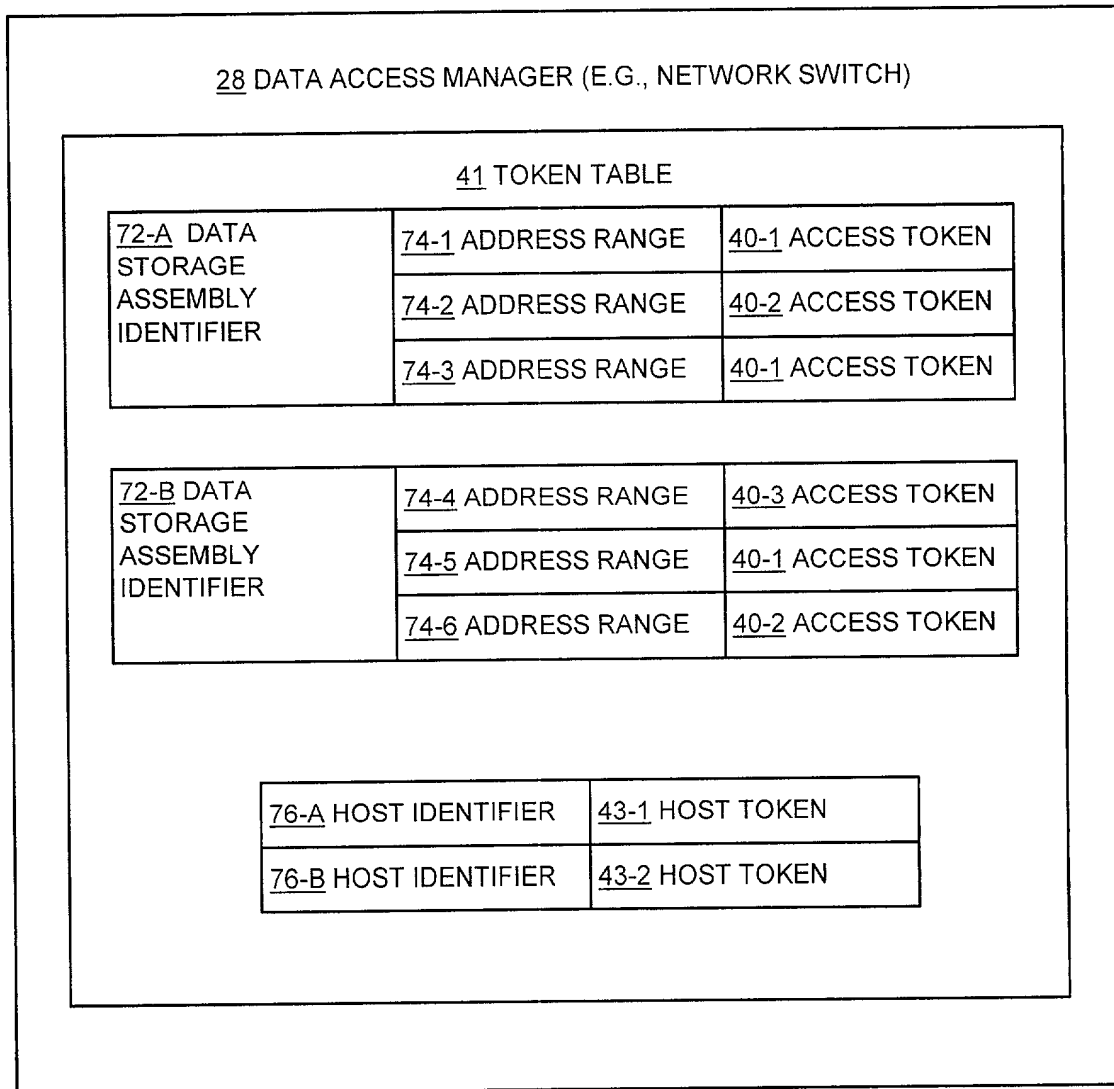


FIG. 10